

Sector No: _____

Side : _____

HS0

Temp (Pt1000-Cesar): _____

Delay: _____

I_mcm= _____ I_bus= _____ [Idet@50V](#)= _____

Min.threshold:

chip0= _____ chip1= _____ chip2= _____ chip3= _____ chip4= _____

chip5= _____ chip6= _____ chip7= _____ chip8= _____ chip9= _____

Uniformity scan

Pre_vth: _____ Test pulse height: _____ Run No: _____

Comment:

HS1

Temp (Pt1000-Cesar): _____

Delay: _____

I_mcm= _____ I_bus= _____ [Idet@50V](#)= _____

Min.threshold:

chip0= _____ chip1= _____ chip2= _____ chip3= _____ chip4= _____

chip5= _____ chip6= _____ chip7= _____ chip8= _____ chip9= _____

Uniformity scan

Pre_vth: _____ Test pulse height: _____ Run No: _____

Comment:

Side : _____

HS2

Temp (Pt1000-Cesar): _____

Delay: _____

I_mcm= _____ I_bus= _____ [Idet@50V](#)= _____

Min.threshold:

chip0= _____ chip1= _____ chip2= _____ chip3= _____ chip4= _____

chip5= _____ chip6= _____ chip7= _____ chip8= _____ chip9= _____

Uniformity scan

Pre_vth: _____ Test pulse height: _____ Run No: _____

Comment:

HS3

Temp (Pt1000-Cesar): _____

Delay: _____

I_mcm= _____ I_bus= _____ [Idet@50V](#)= _____

Min.threshold:

chip0= _____ chip1= _____ chip2= _____ chip3= _____ chip4= _____

chip5= _____ chip6= _____ chip7= _____ chip8= _____ chip9= _____

Uniformity scan

Pre_vth: _____ Test pulse height: _____ Run No: _____

Comment:

Side : _____

HS4

Temp (Pt1000-Cesar): _____

Delay: _____

I_mcm= _____ I_bus= _____ [Idet@50V](#)= _____

Min.threshold:

chip0= _____ chip1= _____ chip2= _____ chip3= _____ chip4= _____

chip5= _____ chip6= _____ chip7= _____ chip8= _____ chip9= _____

Uniformity scan

Pre_vth: _____ Test pulse height: _____ Run No: _____

Comment:

HS5

Temp (Pt1000-Cesar): _____

Delay: _____

I_mcm= _____ I_bus= _____ [Idet@50V](#)= _____

Min.threshold:

chip0= _____ chip1= _____ chip2= _____ chip3= _____ chip4= _____

chip5= _____ chip6= _____ chip7= _____ chip8= _____ chip9= _____

Uniformity scan

Pre_vth: _____ Test pulse height: _____ Run No: _____

Comment:
