# ALICE SPD system installation

# SPD team

Version 0.1 November 21, 2007

This report describes the way the ALICE system components have been installed and tested.

# 1.0 Rack installation

# 2.0 Low/high voltage distribution and electrical cable installation

Figure 1 shows the electrical connection scheme.

@ Describe in more detail.

#### 2.1 C-side electrical cables

Patch panel 3 is sitting directly in the absorber close to the detector. Fig. @ shows the patch panels. The connection is done via printed circuit boards. Each of the LV channels contain a capacitor with the value x uF/ x uF for the 2.6V for the MCM and the 1.8V for the pixel chip power supply. The MCM and bus extenders are connected to patch panel 3. From there (connection 405/410) the LV/HV cables are routed in the absorber cable trays to patch panel 4. Patch panel 4 can be seen in fig. @. Cables 405/410 in Figure 1 have been pre-fabricated and tested before installation. The length of these cables is 4.5m

The cables (407/412a) from PP4 are routed to the LV power supply racks I18 and I19 on a path which is between 33 and 36m long. There the LV cables are connected to the CAEN power supply modules 3009 via PP13, (see Figure 1). The HV cables (412a) go to PP13S and from there on a cable path with a length of xx@ m to CR4 where they are connected directly to the CAEN HV modules xx@.



#### FIGURE 1.

**Electrical connections** 

#### 2.1.1 Test of C-side electrical cables

All LV cables from the LV modules to PP3 have been tested in the follwing way before the half-staves were connected with the MCM and bus extenders. A CAEN 3009 module was connected to the cables corresponding to the full sector. For each channel a different voltage between 1.7V and 1.95V was adjusted so that mixing of cables could have been identified. On the PP3 side a passive load corresponding to the half-stave load was connected (5.5V for the pixel chips and 0.5 A for the MCMs. Furthermore the bias cables and the temperature cables were applied to voltages between 100 and 125V (depending on the channel) and measured on PP3. The voltages on PP3, the connector voltage on the CAEN modules and the current were registered for the MCM and the pixel chips. Table @ shows the measurement protocol of the values taken in PP3. Table @ shows the measurements taken on the I-rack (power supplies).

#### Temperature sensor chain on the pixel bus (PT1000-chain).

The pixel bus PT1000 chain was verified and values were registered, see Table 1 (Pt1000 temp alice)

#### Pt1000 measured in ALICE (7/2007)

Half-stave	Sector 0	Sector 1	Sector 2	Sector 3	Sector 4	Sector 5	Sector 6	Sector 7	Sector 8	Sector 9
0C	22.00	21.64	22.28	23.98	21.99	22.81	22.59	22.31	22.77	23.43
1C	22.49	21.53	22.09	25.63	22.11	21.83	22.49	23.04	23.28	23.49
2C	22.66	21.59	22.04	23.29	22.40	23.56	22.11	21.90	23.73	24.62
3C	22.25	24.12	22.42	22.09	21.89	24.17	27.60	22.13	22.73	24.20
4C	22.18	22.40	21.95	22.28	21.83	21.94	24.27	21.67	23.70	70.73
5C	22.71	21.71	22.39	23.02	21.76	21.95	22.28	21.74	23.85	23.77
Avg	22.3817	22.165	22.195	23.3817	21.9967	22.71	23.5567	22.1317	23.3433	31.7067
stdv	0.28308	1.00933	0.19542	1.29879	0.23269	0.98071	2.12849	0.50539	0.49846	19.1228

#### TABLE 1.

C-side PT1000 chain temperature measurements taken with the 'Cesar box'. (Petra, Simone, Pt1000 temp alice)

The PT1000 chain was again verified using the PLC system. The measurements have been performed after the protection diodes (TVS) have been inserted in PP4 and after the patch panel installation in the I-rack. All values correspond to the expected values with the exception of sector 6 channel 0 and sector 9 channel 4. Table 2 (pt1000sideC) contains the measurements.

#### **Observations:**

Sector 1, MCM5 and sector 4 MCM 5 had a too low voltage and current. This error was later eliminated. The sense wires on the CAEN 3009 were not connected properly.

Sector 6 MCM 0: Defective cable was found for MCM, CAEN module gave HVMAX error. A MCM spare cable is connected instaed.

Sector 9 Bus 0: Cable connector pin on PP4 needed to be inserted properly.

# 3.0 Optical and electrical signal distribution system

Table 2 shows the entire connection scheme.

#### 3.1 C-side optical network

Clock and serial fibers come from CR4 PP7 on link7 directly to PP4. Data (Glink) fibers run from PP4 to a splitter box in C-area (PP5) and from there to the control room on link 11. The lengths of the different sections can be found in table @. It is important that all fibers for the same links have the same length with a tolerance of 60 cm or 3 ns. The fibers for link 6 and 8 must be as short as possible but still must have all the same length.

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Sect 1, Res[2]     5413 194       Sect 1, Res[3]     5461.806       Sect 1, Res[4]     5428.819       Sect 6, Res[1]     5427.083       Sect 6, Res[2]     5420.139       Sect 6, Res[3]     5526.042       Sect 6, Res[4]     5421.875       Sect 6, Res[5]     5461.806       Sect 2, Res[0]     5421.875       Sect 2, Res[1]     5418.402       Sect 2, Res[1]     5418.402       Sect 2, Res[1]     5418.402       Sect 2, Res[1]     5443.013       Sect 2, Res[1]     5443.041       Sect 2, Res[1]     5443.041       Sect 2, Res[2]     5444.04972       Sect 7, Res[3]     5423.611       Sect 7, Res[3]     5423.611       Sect 7, Res[3]     5423.611       Sect 7, Res[3]     5445.012       Sect 7, Res[3]     5446.667       Sect 3, Res[3]     5416.667       Sect 3, Res[3]     5416.667       Sect 3, Res[3]     5416.667       Sect 3, Res[3]     5414.092       Sect 3, Res[3]     5413.025       Sect 4, Res[3]	Sect 1 Res[1]	5411 458		
Sect 1, Res[3]     5401.006       Sect 1, Res[4]     5428.819       Sect 1, Res[5]     5414.931       Sect 6, Res[1]     5427.083       Sect 6, Res[2]     5420.139       Sect 6, Res[3]     5526.042       Sect 6, Res[4]     5421.875       Sect 6, Res[5]     5461.806       Sect 7, Res[6]     5421.875       Sect 2, Res[0]     5421.875       Sect 2, Res[6]     5421.875       Sect 2, Res[6]     5421.875       Sect 2, Res[6]     5423.611       Sect 2, Res[6]     5423.611       Sect 7, Res[0]     5427.083       Sect 7, Res[0]     5427.083       Sect 7, Res[0]     5427.083       Sect 7, Res[0]     5427.083       Sect 7, Res[0]     5443.013       Sect 7, Res[1]     5440.972       Sect 3, Res[0]     5423.611       Sect 3, Res[0]     5423.611       Sect 3, Res[0]     5443.028       Sect 3, Res[5]     5444.0972       Sect 3, Res[5]     5444.028       Sect 3, Res[5]     5444.028       Sect 4, Res[1]	Sect 1 Res[2]	5413 194		
Sect 1, Res[4]   5428 819     Sect 1, Res[5]   5414.931     Sect 6, Res[2]   5427.083     Sect 6, Res[2]   5427.083     Sect 6, Res[2]   5420.139     Sect 6, Res[3]   5526.042     Sect 6, Res[4]   5421.875     Sect 7, Res[5]   5461.806     Sect 2, Res[1]   5418.402     Sect 2, Res[1]   5418.402     Sect 2, Res[2]   5416.667     Sect 2, Res[3]   5423.611     Sect 7, Res[4]   5414.931     Sect 7, Res[3]   5423.611     Sect 7, Res[3]   5423.611     Sect 7, Res[4]   5414.931     Sect 7, Res[3]   5423.611     Sect 7, Res[3]   5423.611     Sect 3, Res[0]   5453.125     Sect 3, Res[3]   5416.667     Sect 3, Res[3]   5416.667     Sect 3, Res[4]   5421.875     Sect 3, Res[5]   5434.028     Sect 4, Res[0]   5416.667     Sect 4, Res[1]   5422.029     Sect 4, Res[3]   5416.667     Sect 4, Res[3]   5416.667     Sect 4, Res[4]   544.972	Sect 1 Res[3]	5461 806		
Sect 1, Res[5]   5414.931     Sect 6, Res[1]   5427.083     Sect 6, Res[2]   5427.083     Sect 6, Res[3]   5526.042     Sect 6, Res[4]   5421.875     Sect 6, Res[5]   5461.806     Sect 7, Res[7]   5418.402     Sect 7, Res[7]   5418.402     Sect 7, Res[7]   5418.402     Sect 7, Res[7]   5418.402     Sect 7, Res[7]   5414.931     Sect 7, Res[7]   5441.931     Sect 7, Res[7]   5444.931     Sect 7, Res[7]   5444.931     Sect 7, Res[7]   5444.931     Sect 3, Res[7]   5443.028     Sect 3, Res[7]   5440.972     Sect 4, Res[7]   5440.972     Sect 4, Res[7]   5442.875     Sect 4, Res[7]   5443.028	Sect 1 Res[4]	5428 819		
Sect 6, Res[1]     6000     overflow     check in DSF log box       Sect 6, Res[2]     5420, 139     5526, 042       Sect 6, Res[3]     5526, 042       Sect 6, Res[4]     5421, 875       Sect 6, Res[5]     5461, 806       Sect 2, Res[0]     5421, 875       Sect 2, Res[1]     5418, 402       Sect 2, Res[2]     5416, 667       Sect 2, Res[3]     5423, 611       Sect 7, Res[4]     5414, 931       Sect 7, Res[1]     5440, 972       Sect 7, Res[2]     5418, 402       Sect 7, Res[3]     5423, 611       Sect 7, Res[4]     5414, 931       Sect 7, Res[5]     5414, 931       Sect 7, Res[2]     5418, 402       Sect 7, Res[2]     5414, 931       Sect 7, Res[2]     5440, 972       Sect 3, Res[1]     5486, 111       Sect 3, Res[2]     5440, 972       Sect 3, Res[3]     5416, 667       Sect 3, Res[4]     5421, 875       Sect 3, Res[5]     5444, 0472       Sect 4, Res[2]     5440, 972       Sect 8, Res[6]     5423, 611 <tr< td=""><td>Sect 1,Res[5]</td><td>5414.931</td><td></td><td></td></tr<>	Sect 1,Res[5]	5414.931		
Sect 6, Res[0]     6000     overflow     check in DSF log box       Sect 6, Res[2]     5420, 139     5526, 042       Sect 6, Res[3]     5526, 042       Sect 6, Res[6]     5421, 875       Sect 6, Res[6]     5421, 875       Sect 7, Res[1]     5418, 402       Sect 2, Res[2]     5416, 6667       Sect 2, Res[2]     5416, 6667       Sect 7, Res[2]     5414, 6667       Sect 7, Res[2]     5414, 802       Sect 7, Res[5]     5423, 611       Sect 7, Res[6]     5441, 4931       Sect 7, Res[6]     5443, 402       Sect 7, Res[6]     5443, 402       Sect 7, Res[6]     5440, 972       Sect 3, Res[6]     5440, 972       Sect 8, Res[6]     5440, 972       Sect 8, Res[6]     5442, 361       Sect 8, Res[6]     5442, 875				
Sect 6, Res[1]   5427.083     Sect 6, Res[2]   5420.139     Sect 6, Res[3]   5526.042     Sect 6, Res[5]   5461.806     Sect 7, Res[5]   5461.806     Sect 2, Res[1]   5418.402     Sect 2, Res[3]   5423.611     Sect 2, Res[4]   5414.331     Sect 2, Res[5]   5423.611     Sect 7, Res[0]   5427.083     Sect 7, Res[1]   5448.402     Sect 7, Res[1]   5443.911     Sect 7, Res[1]   5443.911     Sect 7, Res[1]   5443.911     Sect 7, Res[1]   5443.911     Sect 7, Res[2]   5441.4931     Sect 7, Res[3]   5414.931     Sect 7, Res[4]   5441.931     Sect 7, Res[5]   5423.611     Sect 7, Res[6]   5424.875     Sect 3, Res[2]   5440.972     Sect 3, Res[5]   5443.028     Sect 8, Res[6]   5442.875     Sect 8, Res[6]   5440.972     Sect 8, Res[6]   5444.972     Sect 8, Res[6]   5444.975     Sect 8, Res[6]   5442.875     Sect 8, Res[6]   5442.875	Sect 6,Res[0]	6000	overflow	check in DSF log book
Sect 6, Res[2]   5420.139     Sect 6, Res[4]   5526.042     Sect 6, Res[4]   5421.875     Sect 2, Res[1]   5418.002     Sect 2, Res[2]   5416.667     Sect 2, Res[2]   5418.402     Sect 7, Res[2]   5423.611     Sect 7, Res[0]   5427.083     Sect 7, Res[1]   5440.972     Sect 7, Res[2]   5418.402     Sect 7, Res[3]   5423.611     Sect 7, Res[6]   5421.875     Sect 3, Res[1]   5486.111     Sect 3, Res[2]   5440.972     Sect 3, Res[3]   5416.667     Sect 3, Res[4]   5423.611     Sect 3, Res[5]   5434.028     Sect 8, Res[1]   5432.292     Sect 8, Res[1]   5423.611     Sect 8, Res[2]   5424.875     Sect 8, Res[3]   5421.875     Sect 8, Res[4]   5440.972     Sect 8, Res[5]   5444.444     Sect 8, Res[6]   5423.611	Sect 6,Res[1]	5427.083		
Sect 6, Res[3]   5526.042     Sect 6, Res[4]   5421.875     Sect 2, Res[5]   5461.806     Sect 2, Res[1]   5418.402     Sect 2, Res[2]   5416.667     Sect 2, Res[3]   5423.611     Sect 2, Res[6]   5427.083     Sect 7, Res[1]   5448.402     Sect 7, Res[1]   5444.097     Sect 7, Res[1]   5443.611     Sect 7, Res[2]   5418.402     Sect 7, Res[3]   5423.611     Sect 7, Res[6]   54243.611     Sect 7, Res[6]   5424.4331     Sect 7, Res[6]   543.125     Sect 3, Res[1]   5486.111     Sect 3, Res[2]   5440.972     Sect 3, Res[3]   5416.667     Sect 3, Res[6]   5423.611     Sect 3, Res[6]   5424.0972     Sect 8, Res[2]   5440.972     Sect 8, Res[6]   5424.1875     Sect 8, Res[6]   5424.667     Sect 8, Res[6]   5424.66	Sect 6,Res[2]	5420.139		
Sect 6, Res[4]   5421.875     Sect 2, Res[0]   5421.875     Sect 2, Res[1]   5418.402     Sect 2, Res[2]   5416.667     Sect 2, Res[3]   5423.611     Sect 7, Res[1]   5414.931     Sect 7, Res[1]   5440.972     Sect 7, Res[2]   5418.402     Sect 7, Res[2]   5423.611     Sect 7, Res[2]   5418.402     Sect 7, Res[2]   5414.931     Sect 7, Res[2]   5414.931     Sect 7, Res[2]   5414.931     Sect 7, Res[2]   5414.931     Sect 3, Res[0]   5453.125     Sect 3, Res[1]   5486.111     Sect 3, Res[2]   5440.972     Sect 3, Res[3]   5416.667     Sect 3, Res[5]   5433.028     Sect 8, Res[2]   5440.972     Sect 8, Res[2]   5440.972     Sect 8, Res[3]   5421.875     Sect 8, Res[2]   5422.817	Sect 6,Res[3]	5526.042		
Sect 6, Res[5]   5461.806     Sect 2, Res[0]   5421.875     Sect 2, Res[1]   5418.402     Sect 2, Res[2]   5416.667     Sect 2, Res[3]   5423.611     Sect 2, Res[1]   54243.611     Sect 7, Res[1]   5442.032     Sect 7, Res[1]   5440.972     Sect 7, Res[2]   5418.402     Sect 7, Res[3]   5423.611     Sect 7, Res[1]   5440.972     Sect 7, Res[3]   5423.611     Sect 7, Res[6]   5414.931     Sect 7, Res[7]   5444.931     Sect 3, Res[0]   5453.125     Sect 3, Res[2]   5440.972     Sect 3, Res[2]   5440.972     Sect 3, Res[2]   5440.972     Sect 3, Res[2]   5440.972     Sect 3, Res[6]   5421.875     Sect 3, Res[6]   5422.875     Sect 4, Res[6]   5422.875     Sect 8, Res[1]   5422.875     Sect 8, Res[3]   5421.875     Sect 8, Res[6]   5440.972     Sect 8, Res[6]   5440.972     Sect 8, Res[6]   5440.972     Sect 8, Res[6]   5421.875	Sect 6,Res[4]	5421.875		
Sect 2, Res[0]     5421, 875       Sect 2, Res[1]     5418, 402       Sect 2, Res[2]     5416, 667       Sect 2, Res[3]     5423, 611       Sect 2, Res[4]     5414, 931       Sect 2, Res[7]     5423, 611       Sect 7, Res[7]     5418, 402       Sect 7, Res[7]     5414, 931       Sect 7, Res[7]     5414, 931       Sect 7, Res[7]     5414, 931       Sect 3, Res[0]     5453, 125       Sect 3, Res[1]     5486, 111       Sect 3, Res[2]     5440, 972       Sect 3, Res[3]     5423, 611       Sect 3, Res[2]     5440, 972       Sect 8, Res[1]     5432, 292       Sect 8, Res[2]     5440, 972       Sect 8, Res[2]     5440, 972       Sect 8, Res[3]     5424, 875       Sect 8, Res[3]     5424, 875       Sect 8, Res[3]     5424, 875       Sect 4, Res[1]     5420, 139       Sect 4, Res[3]     5416, 667	Sect 6,Res[5]	5461.806		
Sett 2, Res[1]   5418.402     Sett 2, Res[2]   5418.607     Sett 2, Res[3]   5423.611     Sett 2, Res[6]   5423.611     Sett 2, Res[7]   5423.611     Sett 2, Res[7]   5423.611     Sett 7, Res[1]   5420.083     Sett 7, Res[1]   5420.0972     Sett 7, Res[2]   5418.402     Sett 7, Res[3]   5423.611     Sett 7, Res[4]   5414.931     Sett 7, Res[5]   5414.931     Sett 7, Res[6]   5453.125     Sett 3, Res[1]   5486.111     Sett 3, Res[2]   5440.972     Sett 3, Res[3]   5416.667     Sett 3, Res[5]   5434.028     Sett 3, Res[6]   5422.611     Sett 3, Res[2]   5440.972     Sett 8, Res[3]   5421.875     Sett 8, Res[3]   5423.611     Sett 8, Res[5]   5444.444     Sett 8, Res[5]   5413.194     Sett 4, Res[1]   5423.611	0	E 404 07E		
Sett 2, Res[2]   5416.667     Sett 2, Res[3]   5423.611     Sett 2, Res[6]   5423.611     Sett 2, Res[7]   5443.611     Sett 7, Res[7]   5440.972     Sett 7, Res[7]   5440.972     Sett 7, Res[8]   5423.611     Sett 7, Res[1]   5440.972     Sett 7, Res[2]   5418.402     Sett 7, Res[3]   5423.611     Sett 7, Res[4]   5414.931     Sett 7, Res[5]   5414.931     Sett 3, Res[0]   5453.125     Sett 3, Res[1]   5486.111     Sett 3, Res[2]   5440.972     Sett 3, Res[3]   5414.931     Sett 3, Res[1]   5436.125     Sett 3, Res[2]   5440.972     Sett 3, Res[1]   5422.41.875     Sett 3, Res[1]   5423.611     Sett 8, Res[1]   5423.611     Sett 8, Res[2]   5444.0972     Sett 8, Res[3]   5421.875     Sett 8, Res[3]   5421.875     Sett 4, Res[0]   5414.972     Sett 4, Res[1]   5420.139     Sett 4, Res[2]   5444.444     Sett 4, Res[3]   5413.194 </td <td>Sect 2, Res[U]</td> <td>5421.875</td> <td></td> <td></td>	Sect 2, Res[U]	5421.875		
Sett 2, Res[3]   5410.007     Sett 2, Res[3]   5423.611     Sett 2, Res[4]   5414.931     Sett 7, Res[0]   5427.083     Sett 7, Res[1]   5440.972     Sett 7, Res[2]   5418.402     Sett 7, Res[3]   5423.611     Sett 7, Res[4]   5414.931     Sett 7, Res[5]   5414.402     Sett 7, Res[6]   5414.931     Sett 7, Res[6]   5414.931     Sett 7, Res[6]   5414.931     Sett 3, Res[0]   5453.125     Sett 3, Res[1]   5466.111     Sett 3, Res[1]   5443.028     Sett 3, Res[2]   5440.972     Sett 3, Res[6]   5423.611     Sett 8, Res[1]   5432.292     Sett 8, Res[2]   5440.972     Sett 8, Res[3]   5416.667     Sett 8, Res[4]   5441.875     Sett 8, Res[3]   5423.611     Sett 4, Res[3]   5423.611     Sett 8, Res[4]   5444.444     Sett 8, Res[4]   5443.611     Sett 8, Res[5]   5423.611     Sett 9, Res[3]   5423.611     Sett 9, Res[6]   5414.931	Sect 2, Res[1]	04 18.4UZ		
Sect 2, Res[4]   5414, 931     Sect 2, Res[5]   5423, 611     Sect 7, Res[0]   5427, 083     Sect 7, Res[1]   5440, 972     Sect 7, Res[3]   5423, 611     Sect 7, Res[4]   5414, 931     Sect 7, Res[3]   5423, 611     Sect 7, Res[4]   5414, 931     Sect 7, Res[5]   5414, 931     Sect 7, Res[6]   5453, 125     Sect 3, Res[0]   5453, 125     Sect 3, Res[2]   5440, 972     Sect 3, Res[2]   5440, 972     Sect 3, Res[2]   5440, 972     Sect 3, Res[3]   5416, 667     Sect 3, Res[5]   5434, 028     Sect 8, Res[1]   5432, 611     Sect 8, Res[2]   5440, 972     Sect 8, Res[3]   5412, 875     Sect 8, Res[4]   5441, 0972     Sect 8, Res[5]   5444, 444     Sect 4, Res[0]   5416, 667     Sect 4, Res[1]   5420, 139     Sect 4, Res[3]   5414, 931     Sect 4, Res[4]   5414, 931     Sect 4, Res[5]   5413, 194     Sect 9, Res[6]   5423, 611     Sect 9, Res[6] </td <td>Sect 2 Res[2]</td> <td>5410.00/</td> <td></td> <td></td>	Sect 2 Res[2]	5410.00/		
Sect 2, Res[5]   5423.611     Sect 7, Res[0]   5427.083     Sect 7, Res[1]   5440.972     Sect 7, Res[2]   5418.402     Sect 7, Res[3]   5423.611     Sect 7, Res[6]   5414.931     Sect 7, Res[7]   5414.931     Sect 7, Res[8]   5414.931     Sect 7, Res[9]   5453.125     Sect 3, Res[0]   5453.125     Sect 3, Res[1]   5486.111     Sect 3, Res[2]   5440.972     Sect 3, Res[2]   5440.972     Sect 3, Res[6]   5423.611     Sect 3, Res[1]   5432.292     Sect 8, Res[1]   5432.292     Sect 8, Res[2]   5440.972     Sect 8, Res[3]   5414.444     Sect 8, Res[4]   5440.972     Sect 8, Res[5]   5444.444     Sect 8, Res[6]   5416.667     Sect 4, Res[2]   5425.347     Sect 4, Res[2]   5425.347     Sect 4, Res[3]   5416.667     Sect 4, Res[4]   5414.931     Sect 4, Res[5]   5413.194     Sect 9, Res[1]   5423.611     Sect 9, Res[2]   5444.444	Sect 2, Res[3]	5423.011		
Sect 2, Res[3]   5423.611     Sect 7, Res[0]   5427.083     Sect 7, Res[1]   5440.972     Sect 7, Res[2]   5418.402     Sect 7, Res[3]   5423.611     Sect 7, Res[6]   5414.931     Sect 7, Res[6]   5414.931     Sect 7, Res[6]   5414.931     Sect 3, Res[0]   5453.125     Sect 3, Res[1]   5466.111     Sect 3, Res[2]   5440.972     Sect 3, Res[3]   5416.667     Sect 3, Res[6]   5423.611     Sect 3, Res[1]   5432.292     Sect 8, Res[2]   5440.972     Sect 8, Res[3]   5421.875     Sect 8, Res[3]   5421.875     Sect 8, Res[3]   5421.875     Sect 8, Res[3]   5424.0972     Sect 8, Res[4]   5440.972     Sect 8, Res[5]   5444.444     Sect 4, Res[3]   5416.667     Sect 4, Res[3]   5416.667     Sect 4, Res[4]   5414.931     Sect 4, Res[5]   5413.194     Sect 4, Res[6]   5413.194     Sect 9, Res[0]   5423.611     Sect 9, Res[1]   5423.611	Sect 2, Res[4]	54 14.93 1		
Sect 7, Res[0]   5427.083     Sect 7, Res[1]   5440.972     Sect 7, Res[3]   5423.611     Sect 7, Res[4]   5414.931     Sect 7, Res[5]   5414.931     Sect 3, Res[0]   5453.125     Sect 3, Res[1]   5486.111     Sect 3, Res[2]   5440.972     Sect 3, Res[2]   5440.972     Sect 3, Res[2]   5440.972     Sect 3, Res[3]   5416.667     Sect 3, Res[4]   5421.875     Sect 8, Res[5]   5434.028     Sect 8, Res[1]   5432.292     Sect 8, Res[2]   5440.972     Sect 8, Res[2]   5440.972     Sect 8, Res[2]   5440.972     Sect 8, Res[3]   5421.875     Sect 8, Res[4]   5441.875     Sect 8, Res[5]   5444.444     Sect 4, Res[1]   5420.139     Sect 4, Res[2]   5425.347     Sect 4, Res[3]   5413.194     Sect 9, Res[6]   5413.194     Sect 9, Res[6]   5413.194     Sect 9, Res[1]   5423.611     Sect 9, Res[2]   5434.028     Sect 9, Res[3]   5434.028	Sect 2, Res[5]	0420.011		
Sect 7, Res[1]   5440.972     Sect 7, Res[2]   5418.402     Sect 7, Res[3]   5423.611     Sect 7, Res[4]   5414.931     Sect 7, Res[5]   5413.931     Sect 3, Res[0]   5453.125     Sect 3, Res[1]   5466.111     Sect 3, Res[2]   5440.972     Sect 3, Res[2]   5440.972     Sect 3, Res[2]   5440.972     Sect 3, Res[3]   5416.667     Sect 3, Res[4]   5423.611     Sect 8, Res[1]   5434.028     Sect 8, Res[1]   5423.611     Sect 8, Res[2]   5440.972     Sect 8, Res[2]   5440.972     Sect 8, Res[2]   5440.972     Sect 8, Res[3]   5411.875     Sect 8, Res[4]   5440.972     Sect 4, Res[5]   5444.444     Sect 4, Res[6]   5416.667     Sect 4, Res[6]   5413.187     Sect 4, Res[3]   5413.194     Sect 4, Res[4]   5414.931     Sect 4, Res[5]   5413.194     Sect 9, Res[2]   5434.028     Sect 9, Res[3]   5434.028     Sect 9, Res[3]   5434.028	Sect 7 Res[0]	5427 083		
Sect 7, Res[2]   5418.402     Sect 7, Res[3]   5423.611     Sect 7, Res[5]   5414.931     Sect 7, Res[5]   5414.931     Sect 7, Res[5]   5414.931     Sect 3, Res[1]   5486.111     Sect 3, Res[2]   5440.972     Sect 3, Res[3]   5416.667     Sect 3, Res[4]   5421.875     Sect 3, Res[5]   5434.028     Sect 8, Res[0]   5423.611     Sect 8, Res[1]   5432.292     Sect 8, Res[3]   5416.667     Sect 8, Res[4]   5440.972     Sect 8, Res[3]   5421.875     Sect 8, Res[4]   5440.972     Sect 8, Res[3]   5414.444     Sect 4, Res[3]   5416.667     Sect 4, Res[3]   5416.667     Sect 4, Res[2]   5423.347     Sect 4, Res[2]   5425.347     Sect 4, Res[2]   5423.611     Sect 4, Res[3]   5416.667     Sect 4, Res[4]   5414.931     Sect 4, Res[5]   5413.194     Sect 9, Res[0]   5423.611     Sect 9, Res[1]   5423.611     Sect 9, Res[3]   5434.028	Sect 7 Res[1]	5440 972		
Sect 7, Res[3]     5423.611       Sect 7, Res[4]     5414.931       Sect 7, Res[5]     5414.931       Sect 3, Res[0]     5453.125       Sect 3, Res[1]     5466.111       Sect 3, Res[2]     5440.972       Sect 3, Res[3]     5416.667       Sect 3, Res[5]     5423.611       Sect 3, Res[6]     5423.611       Sect 3, Res[7]     5423.611       Sect 8, Res[7]     5423.611       Sect 8, Res[8]     5421.875       Sect 8, Res[9]     5423.611       Sect 8, Res[1]     5432.292       Sect 8, Res[2]     5440.972       Sect 8, Res[3]     5421.875       Sect 8, Res[4]     5444.444       Sect 4, Res[3]     5416.667       Sect 4, Res[1]     5420.139       Sect 4, Res[2]     5425.347       Sect 4, Res[3]     5413.611       Sect 4, Res[5]     5413.194       Sect 9, Res[6]     5413.194       Sect 9, Res[1]     5423.611       Sect 9, Res[2]     5434.028       Sect 9, Res[3]     5434.028       Sect 9, Res[3]	Sect 7 Res[2]	5418 402		
Sect 7, Res[4]     5414.931       Sect 7, Res[5]     5414.931       Sect 3, Res[0]     5453.125       Sect 3, Res[1]     5486.111       Sect 3, Res[2]     5440.972       Sect 3, Res[3]     5416.667       Sect 3, Res[6]     5423.611       Sect 8, Res[1]     5432.292       Sect 8, Res[1]     5423.611       Sect 8, Res[2]     5440.972       Sect 8, Res[3]     5416.667       Sect 8, Res[4]     5412.875       Sect 8, Res[5]     5444.444       Sect 4, Res[0]     5416.667       Sect 4, Res[1]     5420.139       Sect 4, Res[2]     5423.611       Sect 4, Res[3]     5413.194       Sect 9, Res[4]     5414.931       Sect 9, Res[5]     5434.028       Sect 9, Res[6]     5423.611       Sect 9, Res[1]     5423.611       Sect 9, Res[2]     5434.028       Sect 9, Res[2]     5434.028       Sect 9, Res[2]     5434.028       Sect 9, Res[3]     5434.028       Sect 9, Res[3]     5434.028       Sect 9, Res[3]	Sect 7, Res[3]	5423.611		
Sect 7, Res[5]     5414.931       Sect 3, Res[0]     5453.125       Sect 3, Res[1]     5486.111       Sect 3, Res[2]     5440.972       Sect 3, Res[3]     5416.667       Sect 3, Res[4]     5423.611       Sect 8, Res[0]     5423.611       Sect 8, Res[1]     5432.292       Sect 8, Res[2]     5440.972       Sect 8, Res[3]     5421.875       Sect 8, Res[4]     5440.972       Sect 8, Res[3]     5421.875       Sect 8, Res[4]     5440.972       Sect 8, Res[3]     5444.444       Sect 4, Res[5]     5423.47       Sect 4, Res[1]     5420.139       Sect 4, Res[2]     5425.347       Sect 4, Res[2]     5423.611       Sect 4, Res[3]     5416.667       Sect 4, Res[3]     5413.194       Sect 9, Res[1]     5423.611       Sect 9, Res[2]     5424.414       Sect 9, Res[2]     5434.028       Sect 9, Res[3]     5434.028       Sect 9, Res[3]     5434.028       Sect 9, Res[3]     5434.028       Sect 9, Res[3]	Sect 7. Res[4]	5414.931		
Sect 3, Res[0]   5453.125     Sect 3, Res[1]   5486.111     Sect 3, Res[2]   5440.972     Sect 3, Res[3]   5416.667     Sect 3, Res[0]   5423.611     Sect 8, Res[1]   5432.292     Sect 8, Res[2]   5440.972     Sect 8, Res[2]   5440.972     Sect 8, Res[2]   5440.972     Sect 8, Res[2]   5440.972     Sect 8, Res[3]   5421.875     Sect 8, Res[4]   5440.972     Sect 8, Res[5]   5444.444     Sect 4, Res[0]   5416.667     Sect 4, Res[1]   5423.347     Sect 4, Res[2]   5423.347     Sect 4, Res[3]   5414.931     Sect 4, Res[5]   5413.194     Sect 9, Res[5]   5434.028     Sect 9, Res[2]   5434.028     Sect 9, Res[2]   5434.028     Sect 9, Res[3]   5434.028     Sect 9, Res[3]   5434.028     Sect 9, Res[3]   5434.028	Sect 7.Res[5]	5414.931		
Sect 3, Res[0]   5453.125     Sect 3, Res[1]   5486.111     Sect 3, Res[2]   5440.972     Sect 3, Res[3]   5416.667     Sect 3, Res[4]   5421.875     Sect 3, Res[5]   5434.028     Sect 8, Res[1]   5432.011     Sect 8, Res[1]   5432.021     Sect 8, Res[2]   5440.972     Sect 8, Res[3]   5421.875     Sect 8, Res[2]   5440.972     Sect 8, Res[3]   5421.875     Sect 8, Res[3]   5421.875     Sect 8, Res[3]   5421.875     Sect 8, Res[2]   5440.972     Sect 8, Res[5]   5444.444     Sect 4, Res[0]   5416.667     Sect 4, Res[1]   5420.139     Sect 4, Res[2]   5423.347     Sect 4, Res[3]   5413.194     Sect 9, Res[4]   5414.931     Sect 9, Res[5]   5434.028     Sect 9, Res[6]   5423.611     Sect 9, Res[1]   5423.611     Sect 9, Res[2]   5434.028     Sect 9, Res[3]   5434.028     Sect 9, Res[3]   5434.028     Sect 9, Res[4]   6000				
Sect 3, Res[1]   5486.111     Sect 3, Res[2]   5440.972     Sect 3, Res[3]   5416.667     Sect 3, Res[4]   5421.875     Sect 3, Res[5]   5434.028     Sect 8, Res[0]   5423.611     Sect 8, Res[1]   5432.292     Sect 8, Res[2]   5440.972     Sect 8, Res[3]   5421.875     Sect 8, Res[3]   5421.875     Sect 8, Res[3]   5424.444     Sect 4, Res[0]   5416.667     Sect 4, Res[1]   5420.139     Sect 4, Res[2]   5425.347     Sect 4, Res[2]   5425.347     Sect 4, Res[3]   5416.667     Sect 4, Res[3]   5416.667     Sect 4, Res[4]   5414.931     Sect 4, Res[5]   5413.194     Sect 9, Res[6]   5423.611     Sect 9, Res[5]   5434.028     Sect 9, Res[2]   5444.444     Sect 9, Res[5]   5433.611     Sect 9, Res[6]   5423.611     Sect 9, Res[5]   5434.028     Sect 9, Res[3]   5434.028     Sect 9, Res[5]   5423.611     Sect 9, Res[5]   5423.611	Sect 3,Res[0]	5453.125		
Sect 3, Res[2]   5440.972     Sect 3, Res[3]   5416.667     Sect 3, Res[4]   5421.875     Sect 3, Res[5]   5434.028     Sect 8, Res[0]   5423.611     Sect 8, Res[1]   5432.292     Sect 8, Res[2]   5440.972     Sect 8, Res[3]   5421.875     Sect 8, Res[4]   5440.972     Sect 8, Res[3]   5421.875     Sect 8, Res[3]   5424.444     Sect 4, Res[6]   5444.444     Sect 4, Res[1]   5420.139     Sect 4, Res[2]   5425.347     Sect 4, Res[2]   5425.347     Sect 4, Res[2]   5423.611     Sect 4, Res[3]   5416.667     Sect 4, Res[4]   5414.931     Sect 4, Res[5]   5413.194     Sect 9, Res[6]   5423.611     Sect 9, Res[1]   5423.611     Sect 9, Res[2]   5434.028     Sect 9, Res[3]   5434.028     Sect 9, Res[3]   5434.028     Sect 9, Res[3]   5434.028     Sect 9, Res[5]   5423.611	Sect 3,Res[1]	5486.111		
Sect 3, Res[3]   5416.667     Sect 3, Res[4]   5421.875     Sect 3, Res[5]   5434.028     Sect 8, Res[1]   5432.292     Sect 8, Res[1]   5432.292     Sect 8, Res[2]   5440.972     Sect 8, Res[3]   5421.875     Sect 8, Res[4]   5440.972     Sect 8, Res[5]   5444.444     Sect 4, Res[0]   5416.667     Sect 4, Res[1]   5420.139     Sect 4, Res[2]   5425.347     Sect 4, Res[3]   5414.931     Sect 4, Res[4]   5414.931     Sect 4, Res[5]   5434.028     Sect 9, Res[6]   5423.611     Sect 9, Res[6]   5423.611     Sect 9, Res[1]   5423.611     Sect 9, Res[2]   5434.028     Sect 9, Res[3]   5434.028     Sect 9, Res[5]   5423.611	Sect 3,Res[2]	5440.972		
Sect 3, Res[4]   5421.875     Sect 3, Res[5]   5434.028     Sect 8, Res[0]   5423.611     Sect 8, Res[1]   5432.292     Sect 8, Res[2]   5440.972     Sect 8, Res[3]   5421.875     Sect 8, Res[3]   5421.875     Sect 8, Res[4]   5440.972     Sect 8, Res[5]   5444.444     Sect 4, Res[0]   5416.667     Sect 4, Res[2]   5425.347     Sect 4, Res[2]   5425.347     Sect 4, Res[2]   5425.347     Sect 4, Res[2]   5416.667     Sect 4, Res[2]   5423.611     Sect 9, Res[5]   5413.194     Sect 9, Res[5]   5434.028     Sect 9, Res[1]   5423.611     Sect 9, Res[2]   5444.444     Sect 9, Res[1]   5423.611     Sect 9, Res[2]   5444.444     Sect 9, Res[3]   5434.028     Sect 9, Res[3]   5434.028     Sect 9, Res[3]   5434.028     Sect 9, Res[5]   5423.611     Sect 9, Res[5]   5423.611	Sect 3,Res[3]	5416.667		
Sect 3, Res[5]     5434.028       Sect 8, Res[0]     5423.611       Sect 8, Res[1]     5432.292       Sect 8, Res[2]     5440.972       Sect 8, Res[3]     5421.875       Sect 8, Res[4]     5440.972       Sect 8, Res[5]     5444.444       Sect 4, Res[1]     5420.139       Sect 4, Res[2]     5425.347       Sect 4, Res[2]     5425.347       Sect 4, Res[2]     5416.667       Sect 4, Res[3]     5416.667       Sect 4, Res[4]     5414.931       Sect 4, Res[5]     5413.194       Sect 9, Res[6]     5423.611       Sect 9, Res[1]     5423.611       Sect 9, Res[2]     5434.028       Sect 9, Res[3]     5434.028       Sect 9, Res[3]     5434.028       Sect 9, Res[3]     5434.028       Sect 9, Res[5]     5423.611	Sect 3,Res[4]	5421.875		
Sect 8, Res[0]     5423.611       Sect 8, Res[1]     5432.292       Sect 8, Res[2]     5440.972       Sect 8, Res[3]     5421.875       Sect 8, Res[5]     5444.444       Sect 4, Res[0]     5416.667       Sect 4, Res[1]     5420.139       Sect 4, Res[2]     5425.347       Sect 4, Res[3]     5416.667       Sect 4, Res[4]     5414.931       Sect 4, Res[5]     5413.194       Sect 9, Res[5]     5434.424       Sect 9, Res[1]     5423.611       Sect 9, Res[2]     5434.028       Sect 9, Res[3]     5434.028       Sect 9, Res[3]     5434.028       Sect 9, Res[3]     5434.028       Sect 9, Res[5]     5423.611	Sect 3,Res[5]	5434.028		
Sect 8, Res[0]     5423.611       Sect 8, Res[2]     5432.292       Sect 8, Res[2]     5440.972       Sect 8, Res[3]     5421.875       Sect 8, Res[4]     5440.972       Sect 8, Res[5]     5444.972       Sect 8, Res[6]     5444.972       Sect 4, Res[6]     5444.444       Sect 4, Res[1]     5420.139       Sect 4, Res[2]     5425.347       Sect 4, Res[3]     5416.667       Sect 4, Res[3]     5416.667       Sect 4, Res[4]     5414.931       Sect 4, Res[5]     5413.194       Sect 9, Res[6]     5423.611       Sect 9, Res[1]     5423.611       Sect 9, Res[2]     5444.444       Sect 9, Res[3]     5434.028       Sect 9, Res[3]     5434.028       Sect 9, Res[3]     5434.028       Sect 9, Res[5]     5423.611				
Sect 8, Res[1]   5432.292     Sect 8, Res[2]   5440.972     Sect 8, Res[3]   5421.875     Sect 8, Res[4]   5440.972     Sect 8, Res[5]   5441.444     Sect 4, Res[1]   5420.139     Sect 4, Res[2]   5425.347     Sect 4, Res[2]   5425.347     Sect 4, Res[3]   5416.667     Sect 4, Res[4]   5414.931     Sect 4, Res[5]   5413.194     Sect 9, Res[6]   5423.611     Sect 9, Res[1]   5423.611     Sect 9, Res[2]   5444.444     Sect 9, Res[3]   5433.028     Sect 9, Res[3]   5434.028     Sect 9, Res[3]   5434.028     Sect 9, Res[5]   5423.611	Sect 8,Res[0]	5423.611		
Sect 8, Res[2]     5440.972       Sect 8, Res[3]     5421.875       Sect 8, Res[4]     5440.972       Sect 8, Res[5]     5444.444       Sect 4, Res[0]     5416.667       Sect 4, Res[1]     5420.139       Sect 4, Res[2]     5425.347       Sect 4, Res[3]     5416.667       Sect 4, Res[3]     5416.667       Sect 4, Res[4]     5414.931       Sect 4, Res[5]     5413.194       Sect 9, Res[5]     5423.611       Sect 9, Res[1]     5423.611       Sect 9, Res[2]     5434.028       Sect 9, Res[3]     5432.611       Sect 9, Res[5]     5423.611	Sect 8,Res[1]	5432.292		
Sect 8,Res[3]     5421.875       Sect 8,Res[4]     5440.972       Sect 8,Res[5]     5444.444       Sect 4,Res[1]     5420.139       Sect 4,Res[1]     5425.347       Sect 4,Res[2]     5425.347       Sect 4,Res[3]     5416.667       Sect 4,Res[4]     5414.931       Sect 4,Res[5]     5413.194       Sect 9,Res[0]     5423.611       Sect 9,Res[1]     5423.611       Sect 9,Res[2]     5444.444       Sect 9,Res[3]     5434.028       Sect 9,Res[3]     5434.028       Sect 9,Res[5]     5423.611       Sect 9,Res[5]     5423.611	Sect 8,Res[2]	5440.972		
Sect 8, Res[4]     5440.972       Sect 8, Res[5]     5444.444       Sect 4, Res[0]     5416.667       Sect 4, Res[2]     5425.347       Sect 4, Res[3]     5416.667       Sect 4, Res[4]     5414.931       Sect 4, Res[5]     5413.194       Sect 9, Res[6]     5423.611       Sect 9, Res[1]     5423.611       Sect 9, Res[3]     5434.028       Sect 9, Res[3]     5434.028       Sect 9, Res[5]     5423.611       Sect 9, Res[5]     5423.611	Sect 8,Res[3]	5421.875		
Sect 8, Res[0]     5444.444       Sect 4, Res[0]     5416.667       Sect 4, Res[1]     5420.139       Sect 4, Res[2]     5425.347       Sect 4, Res[3]     5416.667       Sect 4, Res[3]     5416.667       Sect 4, Res[3]     5416.667       Sect 4, Res[3]     5414.931       Sect 4, Res[5]     5413.194       Sect 9, Res[0]     5423.611       Sect 9, Res[1]     5423.611       Sect 9, Res[2]     5444.444       Sect 9, Res[3]     5434.028       Sect 9, Res[3]     5434.028       Sect 9, Res[5]     5423.611	Sect 8,Res[4]	5440.972		
Sect 4, Res[0]     5416.667       Sect 4, Res[1]     5420.139       Sect 4, Res[2]     5425.347       Sect 4, Res[3]     5416.667       Sect 4, Res[3]     5416.667       Sect 4, Res[4]     5414.931       Sect 4, Res[5]     5413.194       Sect 9, Res[0]     5423.611       Sect 9, Res[1]     5423.611       Sect 9, Res[2]     5444.444       Sect 9, Res[3]     5434.028       Sect 9, Res[4]     6000       Sect 9, Res[5]     5423.611	Sect 8,Res[5]	5444.444		
Sect 4, Res[1]     5420.139       Sect 4, Res[2]     5425.347       Sect 4, Res[3]     5416.667       Sect 4, Res[4]     5414.931       Sect 4, Res[5]     5413.194       Sect 9, Res[0]     5423.611       Sect 9, Res[1]     5423.611       Sect 9, Res[2]     5444.444       Sect 9, Res[3]     5434.028       Sect 9, Res[5]     5423.611	Soct 4 Bos[0]	5416 667		
Sect 4, Res[2]     5425, 347       Sect 4, Res[2]     5425, 347       Sect 4, Res[3]     5416, 667       Sect 4, Res[4]     5414, 931       Sect 4, Res[5]     5413, 194       Sect 9, Res[6]     5423, 611       Sect 9, Res[2]     5444, 444       Sect 9, Res[3]     5434, 028       Sect 9, Res[3]     5434, 028       Sect 9, Res[5]     5423, 611	Sect 4, Res[U]	5410.007		
Sect 4, Res[3]     5416.667       Sect 4, Res[4]     5414.931       Sect 4, Res[5]     5413.194       Sect 9, Res[0]     5423.611       Sect 9, Res[1]     5423.611       Sect 9, Res[2]     5444.444       Sect 9, Res[3]     5434.028       Sect 9, Res[3]     5434.028       Sect 9, Res[5]     5423.611	Sect 4 Pos[2]	5420.139 542F 247		
Sect 4, Res[4]     5414,931       Sect 4, Res[5]     5413,194       Sect 9, Res[0]     5423,611       Sect 9, Res[1]     5423,611       Sect 9, Res[2]     5444,444       Sect 9, Res[3]     5434,028       Sect 9, Res[5]     5423,611       Sect 9, Res[3]     5434,028       Sect 9, Res[5]     5423,611	Sect 1 Res[3]	0420.047 5/16 667		
Sect 4, Res[5]     5413.194       Sect 9, Res[6]     5423.611       Sect 9, Res[1]     5423.611       Sect 9, Res[2]     5444.444       Sect 9, Res[3]     5434.028       Sect 9, Res[3]     5433.028       Sect 9, Res[5]     5423.611	Sect 4 Res[0]	5/1/ 031		
Sect 9,Res[0]     5423.611       Sect 9,Res[1]     5423.611       Sect 9,Res[2]     5444.444       Sect 9,Res[3]     5434.028       Sect 9,Res[4]     6000     6200 Ohm       Sect 9,Res[5]     5423.611	Sect 4 Res[5]	5413 194		
Sect 9,Res[0]     5423.611       Sect 9,Res[1]     5423.611       Sect 9,Res[2]     5444.444       Sect 9,Res[3]     5434.028       Sect 9,Res[4]     6000       Sect 9,Res[5]     5423.611	00017,100[0]	5-13.134		
Sect 9,Res[1]     5423.611       Sect 9,Res[2]     5444.444       Sect 9,Res[3]     5434.028       Sect 9,Res[4]     6000     6200 Ohm       Sect 9,Res[5]     5423.611	Sect 9,Res[0]	5423.611		
Sect 9, Res[2]     5444.444       Sect 9, Res[3]     5434.028       Sect 9, Res[3]     6000       Sect 9, Res[5]     6000       Sect 9, Res[5]     5423.611	Sect 9,Res[1]	5423.611		
Sect 9, Res[3]     5434.028       Sect 9, Res[4]     6000     6200 Ohm       Sect 9, Res[5]     5423.611	Sect 9,Res[2]	5444.444		
Sect 9, Res[4]     6000     6200 Ohm       Sect 9, Res[5]     5423.611	Sect 9,Res[3]	5434.028		
Sect 9,Res[5] 5423.611	Sect 9,Res[4]	6000	6200 Ohm	
	Sect 9,Res[5]	5423.611		

TABLE 2.

C-side PT1000 chain temperature measurements taken with the PLC system (M. Caselle, pt1000sideC-3).



#### FIGURE 2.

Data connections on optical and electrical cables.

From PP4 all optical signals are on patch cords in the cable trays on connected via the optical patch panel PP3D to the half-stave pig tails. A mixing-up of fibers in the installation in CR4, was equalized by renaming the patch cords on PP3 accordingly. As a result patch cord connections in PP4 are not in the correct place for the fibers concerned. Table @ shows the fibers and the change in the labelling.

In addition during the connection to the half-stave pig tails some fibers were found defective and spare fibers have been used instead and are outlined as follows:

#### **Observations:**

Sector 3 Serial 0 replaced for Z0 (broken connector on PP3)

Sector 4 Data 4 replaced for Z2

Sector 7 Data 2 replaced for Z2

The initial installation of the data fibers from PP4 to PP5 did not take into account that all fibers must have the same length and must be as short as possible. For that reason the fibers were cut in the C-area, the routing path was shortened and the fibers were re-spliced in a so called splicing box (PP5a, Figure 2). Table @ shows the length of each indivial cable coming from PP4 and going to PP5.

The initial installation of the clock&serial fibers on the C-side did not take into account that all fibers must have the same length. Table @ (mail Florian Oct 15) shows the length of the 10 cables from CR4 PP7 to the ten PP4 boxes. Additional cables are spliced in the Rack Y01 in CR4 to equalize this length.

#### 3.1.1 Test of C-side optical connections

All MCMs on the C-side have been powered one by one. Clock, serial and data fibers have been connected with optical attenuators (clock 6dB, serial 5dB, data 8dB) to a universal SPD test board (MPT) and communication to the MCM has been established and verified by reading the id-registers of the MCM chips. Table @ shows the measurement protocol.

The MCM power supply currents as well as the sensor reverse current at 2 and 5V were recorded and can be seen in table @.

After a cooling leak in the silicon drift detector sensor leakage tests were repeated for a subset of half-staves. The results are shown in Table 3 (Smes09Aug07-1.xls)

#### **Observations.**

Sector 3 MCM1: serial signal was changed to spare signal Z0

Sector 4 MCM4: data was changed to spare signal Z2.

Sector 5 MCM2: data was changed to spare signal Z2.

Sector 7 MCM2: data was changed to spare signal Z2.

Sector 7 MCM3: serial was changed to Z1

Sector 8 MCM0: data was changed to spare signal Z2.

During the test the connector sector 6 Data 0 was found defective on PP4: data was changed to spare signal Z2.

Fig. @ shows the entire connection scheme.



TABLE 3.

Sensor leakage and forward tests (Petra, Simone, Smes09Aug07-1.xls)

# 3.2 A-side optical network

Clock and serial fibers come from CR4 PP8 on link 8 to PP0S. From there prefabricated optical cables connect via the mini-frame to PP1. Data (G-link) fibers run from PP8 in CR4 to a splitter box in C-area (PP5) and from to PP0T under the space frame. From there prefabricated cables connect to PP1. The lengths of the different sections can be found in table @. It is important that all fibers for the same links have the same length with a tolerance of 60 cm or 3 ns. The fibers for link 6 and 8 must be as short as possible but still must have all the same length.

From PP1 all optical signals are on patch cords in the cable trays on the service chariot connected via the optical patch panel PP2D to the half-stave pig tails.

#### 3.2.1 Test of A-side optical connections

All MCM were switched on one by one using the cables coming out of the service chariotm and which will be connected to PP1. Clock, serial and data ware connected to the SPD MCM tester (MPT) with a 6 dB attenuator each. The MCM id registers were read and compared automatically to the expected value. Furthermore it was verified that the bias forward for all ladders was between @ and @. The reverse current for @ V was measured. Table 4 (MCMtestAside Oct 4) reports the test results.



TABLE 4.

MCM test, PT1000 chain measurement, sensor current and pixel chip power supply resistance for A-side on cables coming out of service chariot. (MCMtestAside Oct 4, MC)

#### **Observations:**

Sector1 MCM0 - Sectpr2 MCM0: All optical fibers (clock, serial, data) of Sector 1 MCM 0 and Sector 2 MCM 0 are connected to the respective other MCM. The inversion has been done on the level of the pig tails on PP2.

Sector A1 MCM5: Data has been conected to spare Z2.

Sector A8 channel5: leakage current 1.9 uA.

Sector A9 channel5: Pt1000 chain is not connected.

# 4.0 Temperature interlock (PLC)

# 4.1 PLC in I-rack for C-side

Figure @ shows a photograph of the installation. Figure @ shows the setup in the I-rack @.(PLC rack layout 23.oct)

All channels had been tested. The CAEN modules were powered but no channel was switched on. By changing the threshold in the PLC each of the 60 outpus was forced to send an interlock to the CAEN 3009 module. The arrival of the signal was verified by looking at the LED on the 3009 module.

Observation:

In the PLC code (Cesar code) the option was implemented to remove a channel from the interlock as there are at least two channels on the C-side and one on the A-side which have a non-working Pt1000 chain. These channels are:

Sector C6 channel0

Sector C9 channel4

Sector A9 channel5

# 5.0 Patch panels

# 5.1 Optical patch panel PP7 for the C-side in CR4

The layout of the signal connection of the optical patch panel 7 for the C-side in CR4 can be found in Figure 3 (CR4 opt P7/8legend). Note, that due to the mixing up during the cabling the fiber connections are not consequently in order.

CR4 optical patch panel PP7, Nov 5, 2007, AK, MC 2 3 4 5 6 9 0 1 7 8 Ζ Ζ PP .0 0.0/0.1 .0/1. 2.0/2.1 3.0/3.1 4.0/4.1 5.0/5.1 6.0/6.1 7.0/7.1 8.0/8.1 9.0/9.1 4.2/4.3 0.2/0.3 .2/1.3 2.2/2.3 3.2/3.3 5.2/5.3 6.2/6.3 8.2/8.3 9.2/9.3 7.2 Clock 0.4/0.5 .4/1.5 2.4/2.5 3.4/3.5 4.4/4.5 5.4/5.5 6.4/6.5 7.4/7.5 8.4/8.5 9.4/9.5 0.0/0.1 1.0/1.1 2.0/2.1 3.0/2 4.0/4.1 5.0/5.1 6.0/6.1 7.0/7. 8.0/8.1 9.0/9.1 Serial 3.2/3.3 4.2/4.3 0.2/0.3 1.2/1.3 2.2/2.3 5.2/5.3 6.2/6.3 7.2/7.3 8.2/8.3 9.2/9.3 Z0/8.Z1 3.4/3.5 4.4/4.5 5.4/5 8.4/8.5 0.4/0.5 1.4/1.5 2.4/2.5 6.4/6.5 7.4/7.5 9.4/9 Connection Spare: Serial & Clock **Connection 10** PR DDL DCDC **PP7.1** 3.0/3.1 5.0/5.1 0.0/9.14.5/4 .0/7.\* 0 3.2/3.3 5.2/5.3 9.2/9.3 23 7.2/7.3 2.5/ **G-link** 3.4/3.5 5.4/5.5 9.4/9.5 .4/7.5 0.5/0.Z 45 Z2/0 3.Z2/2.0 5.Z2/4 7 721 9 72/0 8.5/2. 8.1/8.2 Z2/2.2 1/0 2 4.1/4.26.1/6.2 6.5/ PH 3/0.4 2.3/2.4 8.3/8.4 6.3/6.4 Conn 20 Conn 18 striked trough fibers D,C,S are broken and have been replaced by Z fibers, on detector lug side swap has been made on PP3 Conn 12

Patch panels

# FIGURE 3.

PP7 connection in CR4 - C-side (CR4 opt P7/8legend).

# 5.2 Optical patch panel PP8 for the A-side in CR4

The layout of the signal connection of the optical patch panel 7 for the A-side in CR4 can be found in figure @.

# 5.3 Optical patch panel PP5 (splitter box) in C-area

The layout of the signal connection of the optical patch panel 5 (splitter box) for can be found in figure @ (5 Nov). Note, that due to the mixing up during the cabling the fiber connections are not consequently in order.

# Patch panels



FIGURE 4.

PP5 splitter box optical connections (Florian, PP5optLegend).



Absorber measurements sector 0.

TABLE 5.

ALICE SPD system installation



TABLE 7.

Absorber measurements sector 1.

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lebels an lous extends road sector 4,

ALICE SPD system installation



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Measure on one by one HV sensor and confirm
Measure on one by one temperature sensor and confirm after each channel
Connect Bus and MCM to detector
Connect Buser for proper routing & clean optics & connect optics
close PP3

an ok LX 5 KXXXO

Absorber measurements sector 3.



ALICE SPD system installation



Absorber measurements sector 5.



Absorber measurements sector 6.

ALICE SPD system installation



TABLE 19.

Absorber measurements sector 7.



Absorber measurements sector 8.

TABLE 21.



Absorber measurements sector 9.



TABLE 25.

Rack I measurements sector 0.



TABLE 27.

Rack I measurements sector 1.

				: (
1) Swi 2) Swi 3) Con 5) Con		HS	Sector	Rack I Side C
tch on MC trh off all; nnect Ande nnect temp nnect temp (H		Bus Nor Voltage V	8	Cable
M&Bus arson-sh Bias to ( Derature	$\begin{array}{c c}95 & 4 \\ 1.95 & 4 \\ 1.85 & 4 \\ 1.75 & 4 \\ 1.77 & 4 \\ \end{array}$	ninal Com Volta	(Hors	Testing
たい Sensor HCL HCL HCL HCL HCL HCL HCL HCL	20 24 24 24 24 24 24 24 24 24 24 24 24	nector age	t t	& conne
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Anni Anni tor I values al voltag al voltag (Cuu Voltag (Cuu	2.85 2.8 2.75 2.75 2.65 2.65 2.6	ge al		
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) abosrb it for co wait for 00 X	00000	MCM cu		
er confirmati	- 00 00 00 - 0	Obs rrent s		
ion & co nation		servation		
nnect H	4.74 4.63 4.26 4.25 4.25	Bias curr		
IV short	2 MAR A A A A A A A A A A A A A A A A A A A	ent bias v		
: circuite	125 120 115 115 105	/oltage cu		1200 1200 1200 1200 1200
2	Along Son Son	emp urrent		
		Temp Voltage	M:R	
	125 120 115 100	w	5/2.	
			7.2005	

TABLE 29.

Rack I measurements sector 2.

ALICE SPD system installation



TABLE 31.

Rack I measurements sector 3.



TABLE 33.

Rack I measurements sector 4.

ALICE SPD system installation



.

Rack I measurements sector 5.

TABLE 35.



TABLE 37.

Rack I measurements sector 6.

ALICE SPD system installation

Patch panels



Patch panels

TABLE 39.

Rack I measurements sector 7.

ALICE SPD system installation



Measure on one by one HV sensor and confirm
Measure on one by one temperature sensor and confirm after each channel
Connect Bus and MCM to detector
Use laser for proper routing & clean optics & connect optics
close PP3

Rack I measurements sector 8.

TABLE 41.

ALICE SPD system installation



Rack I measurements sector 9.

TABLE 43.

ALICE SPD system installation



TABLE 45.

MCM test protocol CR4 C-side sector 0.



TABLE 47.

MCM test protocol CR4 C-side sector 1.

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TABLE 49.

MCM test protocol CR4 C-side sector 2.



TABLE 51.

MCM test protocol CR4 C-side sector 3.



TABLE 53.

MCM test protocol CR4 C-side sector 4.



TABLE 55.

MCM test protocol CR4 C-side sector 5.

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TABLE 57.

MCM test protocol CR4 C-side sector 6.



TABLE 59.

AMCM test protocol CR4 C-side sector 7.



.

0) open PP3 1) Measure all MCM & Bus voltages

Measure on one by one HV sensor and confirm
Measure on one by one temperature sensor and confirm after each channel
Connect Bus and MCM to detector
Use laser for proper routing & clean optics & connect optics
close PP3

TABLE 61.

MCM test protocol CR4 C-side sector 8.



Patch panels

MCM test protocol CR4 C-side sector 9.

TABLE 63.



Rack I measurements for MCM on C-side sector 0.

TABLE 65.



TABLE 67.

Rack I measurements for MCM on C-side sector 1.





Rack I measurements for MCM on C-side sector 2.



TABLE 71.

Rack I measurements for MCM on C-side sector 3.





Rack I measurements for MCM on C-side sector 4.



TABLE 75.

Rack I measurements for MCM on C-side sector 5.





Rack I measurements for MCM on C-side sector 6.



TABLE 79.

Rack I measurements for MCM on C-side sector 7.

ALICE SPD system installation



Rack I measurements for MCM on C-side sector 8.



TABLE 83.

Rack I measurements for MCM on C-side sector 9.



TABLE 85.

Optical patch panel connection PP3 (Page1).

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Optical patch panel connection PP3 (Page2).

ALICE SPD system installation



TABLE 89.

Optical patch panel connection PP3 (Page 3).

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TABLE 91.

Optical patch panel connection PP3 (Page 4).



TABLE 93.

Optical patch panel connection PP3 (Page 5).

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